

**RESET MODE INDICATION FOR AN INTEGRATED CIRCUIT USING A NON-  
DEDICATED PIN**  
**ABSTRACT OF THE DISCLOSURE**

An integrated circuit has a circuitry and pins coupled to the circuitry.  
One of the pins is a non-dedicated reset pin having a pin signal that is set at a  
level outside of a normal range for the pin signal so that the integrated circuit  
5 is indicated to reset.